EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2446	257/296-313,533,595-602,923,924)/.ccls. and (capacitor with trench)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 15:50
L2	900	257/296-313,533,595-602,923,924)/.ccls. and (MOS) and (capacitor with trench)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 15:51
L3	167	257/296-313,533,595-602,923,924)/.ccls. and (MOS) and (capacitor with trench) and (coated or plated)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 16:04
L4	66	257/296-313,533,595-602,923,924)/.ccls. and (MOS) and (capacitor with trench) and ((coated or plated) with (gate ajd oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 16:08
L5	46	257/296-313,533,595-602,923,924)/.ccls. and ((MOS) with transistor) and (capacitor with trench) and ((coated or plated) with (gate ajd oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 17:13

L6	12	257/e27.016, e27.017,e27.019- e27.021,e27.023- e27.025,e27.03- e27.035,e27.037, e27.038,e27.041- e27.045,e27.047, e27.048,e27.071, e27.09,e27.092, e27.093,e27.095, e27.101,e27.114- e27.116)/.ccls. and ((MOS) with transistor) and (capacitor with trench) and ((coated or plated) with (gate ajd oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 17:41
L7	0	(SRAM with (two adj inverters)) and ((MOS) with transistor) and (capacitor with trench) and ((coated or plated) with (gate ajd oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 18:02
L8	0	(SRAM with (two adj inverters)) and ((MOS) with transistor) and (capacitor with trench) and ((coated or plated) with (gate adj oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 18:03
L9	0	(SRAM with inverters) and ((MOS) with transistor) and (capacitor with trench) and ((coated or plated) with (gate adj oxide))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/13 18:04

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